

Docket No.: M4065.0624/P624

(PATENT)

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Howard E. Rhodes

Application No.: 10/751,439

Confirmation No.: 3917

Filed: January 6, 2004

Art Unit: 2818

For: METHOD AND APPARATUS PROVIDING

CMOS IMAGER DEVICE PIXEL WITH

TRANSISTOR HAVING LOWER

THRESHOLD VOLTAGE THAN OTHER

IMAGER DEVICE TRANSISTORS

Examiner: D. Nhu

AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

MS AF Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated May 19, 2006, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 12 of this paper.